

FIG. 1
(PRIOR ART)

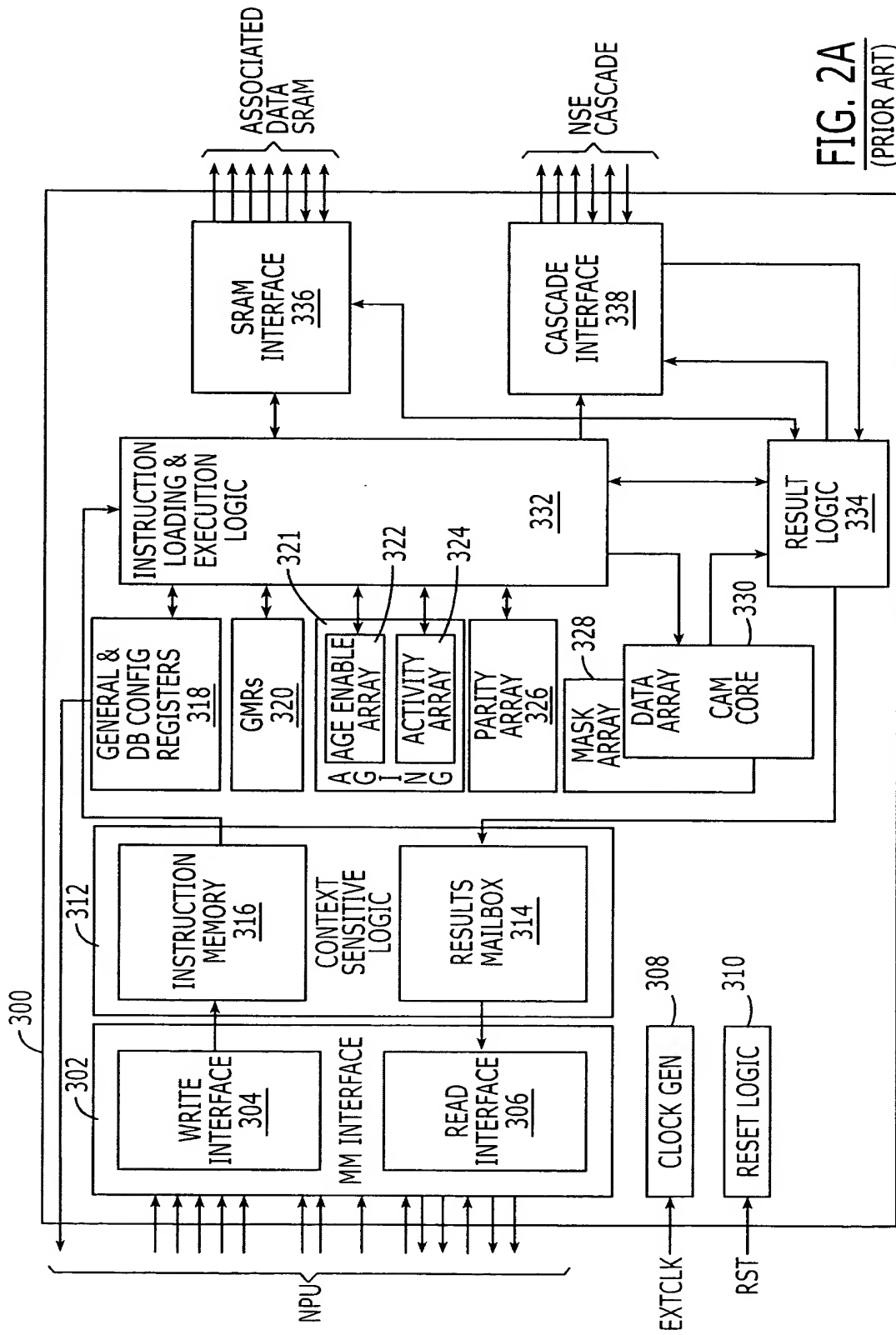


FIG. 2A
(PRIOR ART)

Dev ID	Segment	Segment Entry Offset
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FIG. 2B

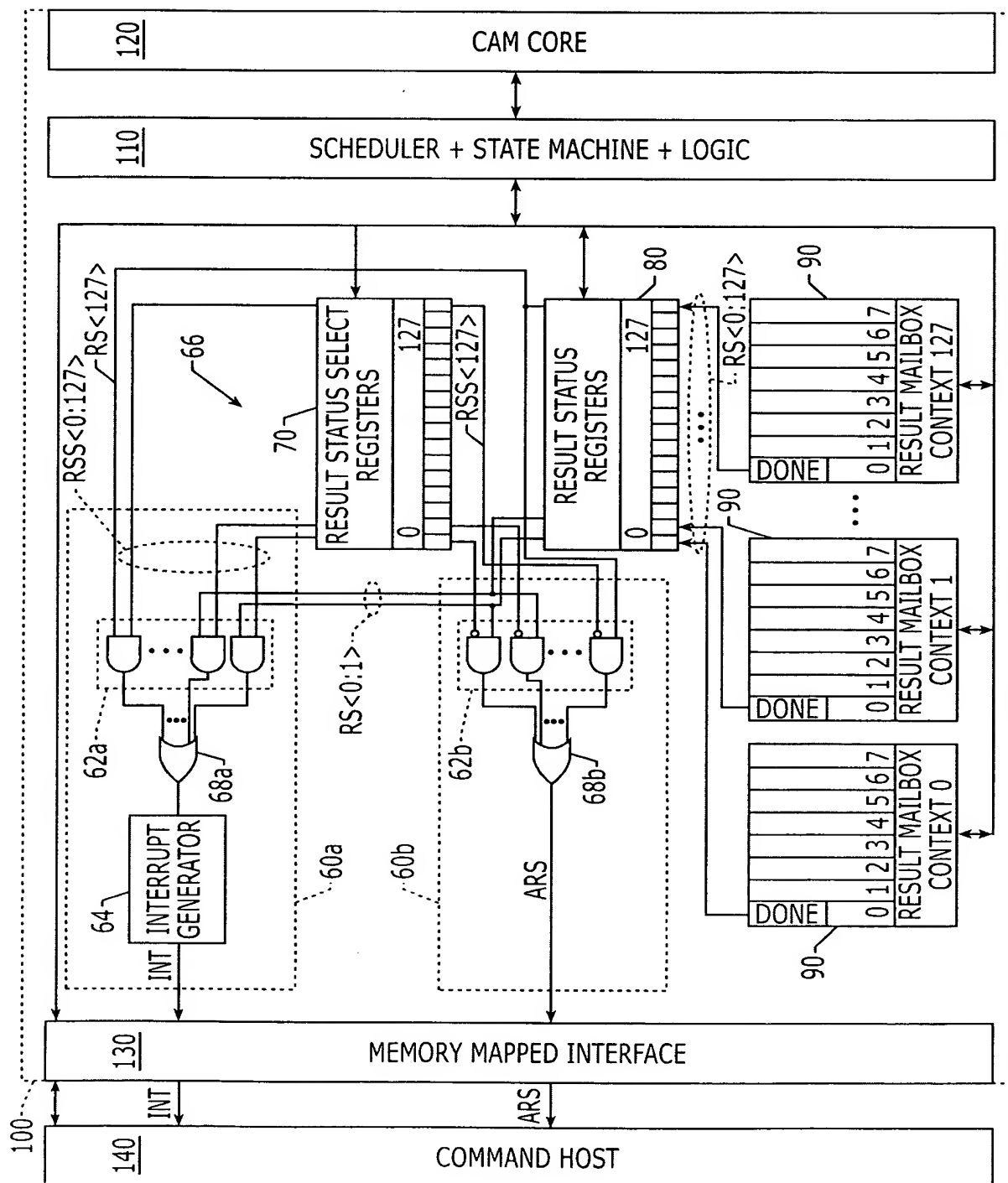


FIG. 3

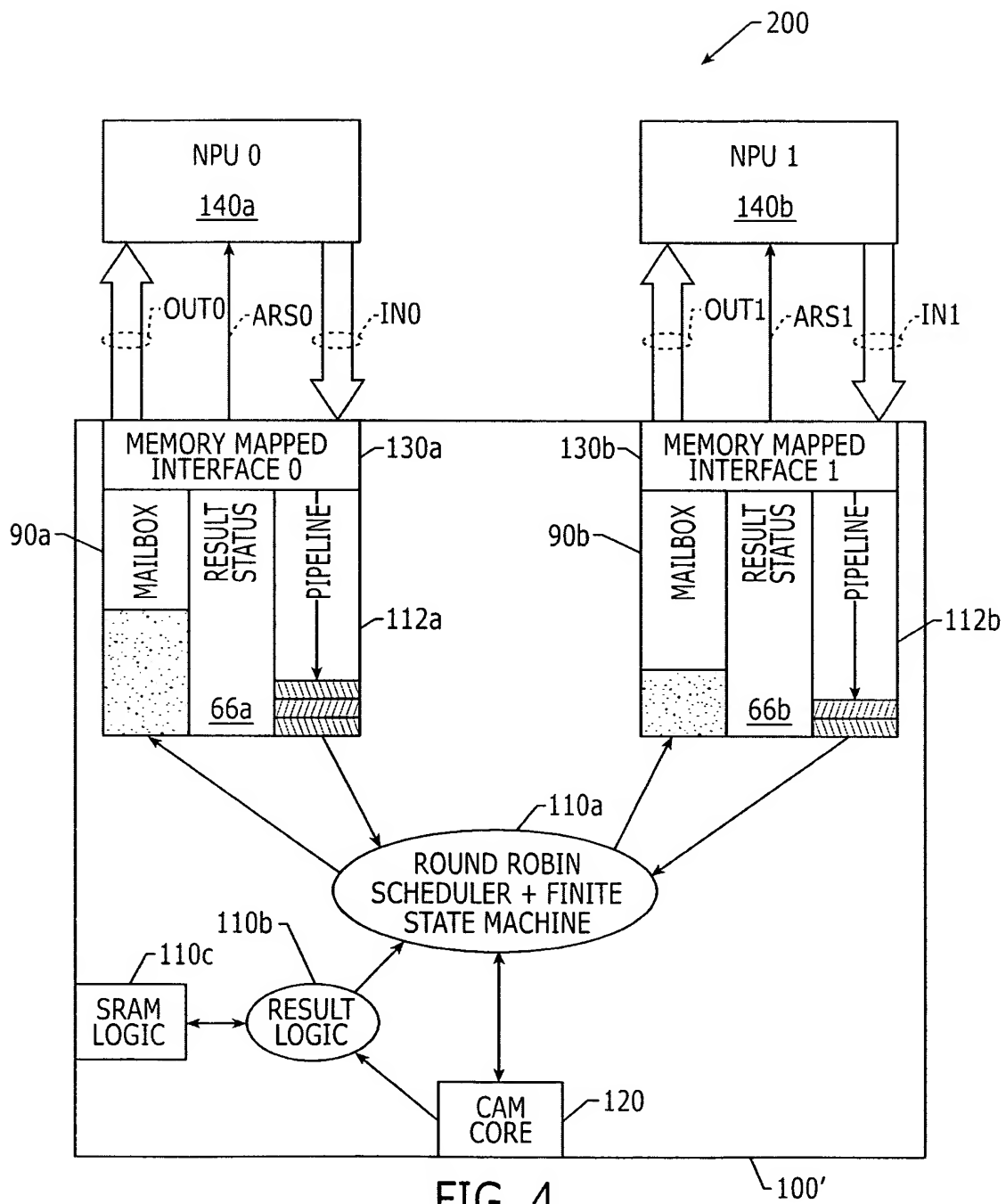


FIG. 4

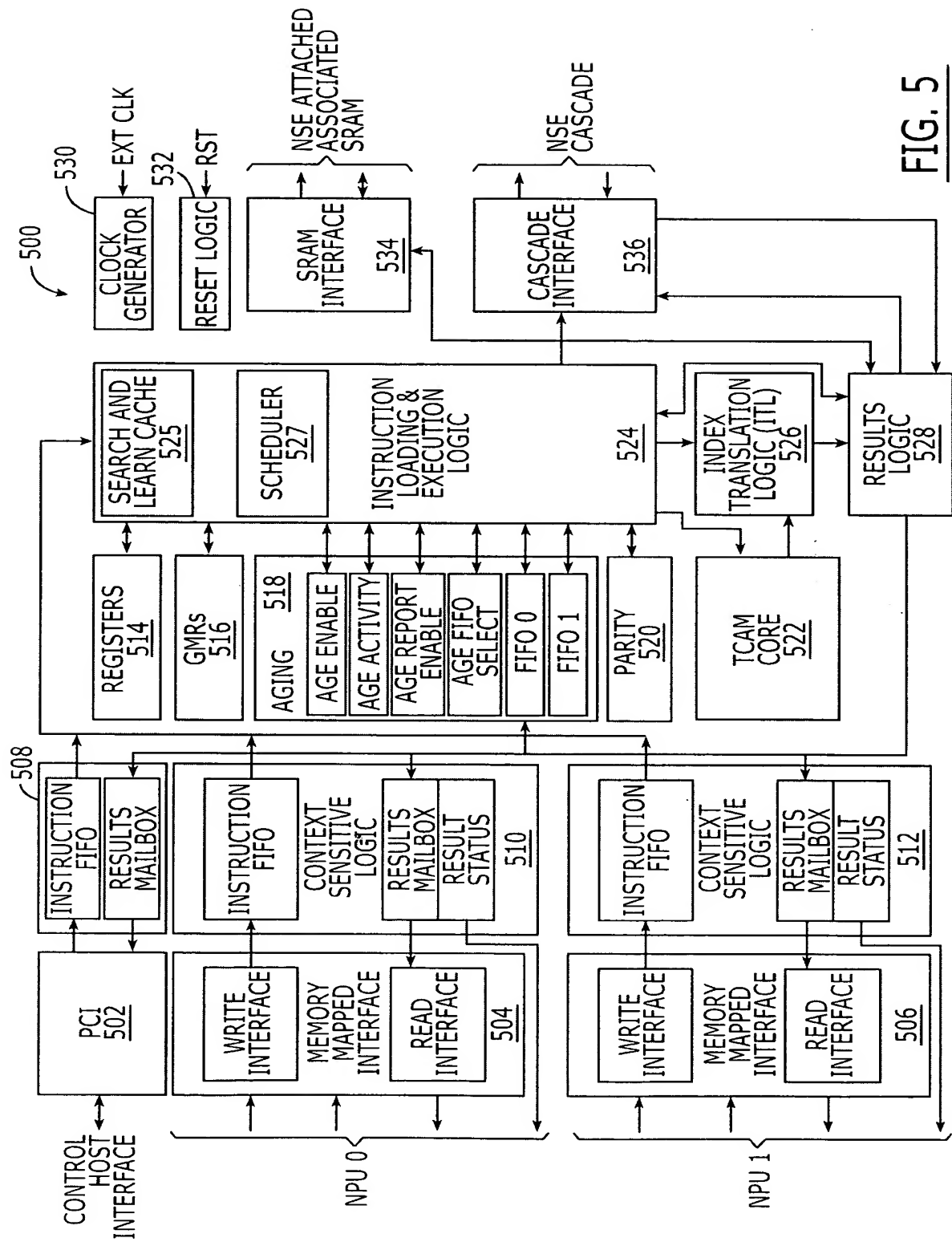


FIG. 5

FIG. 6A
FIG. 6B

FIG. 6

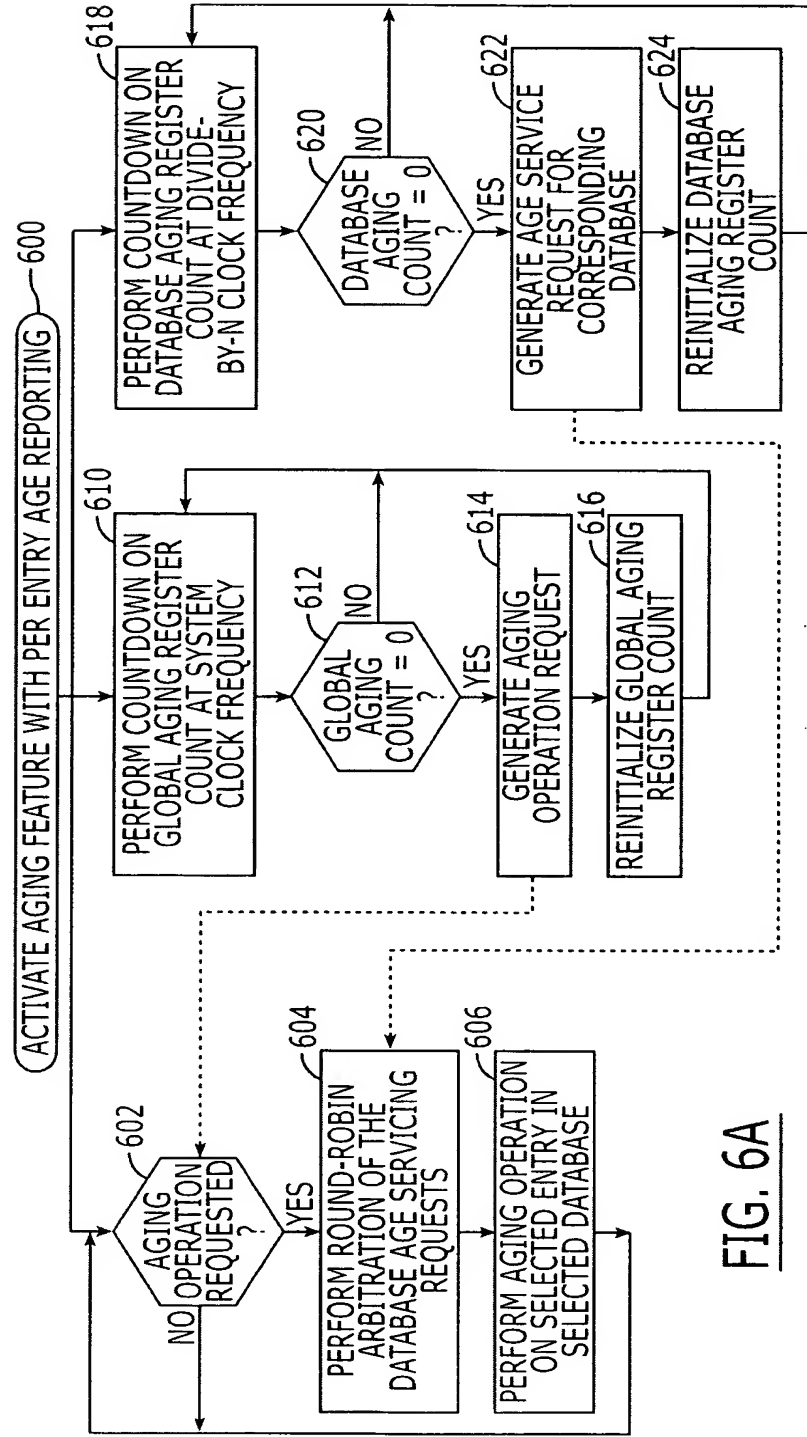


FIG. 6A

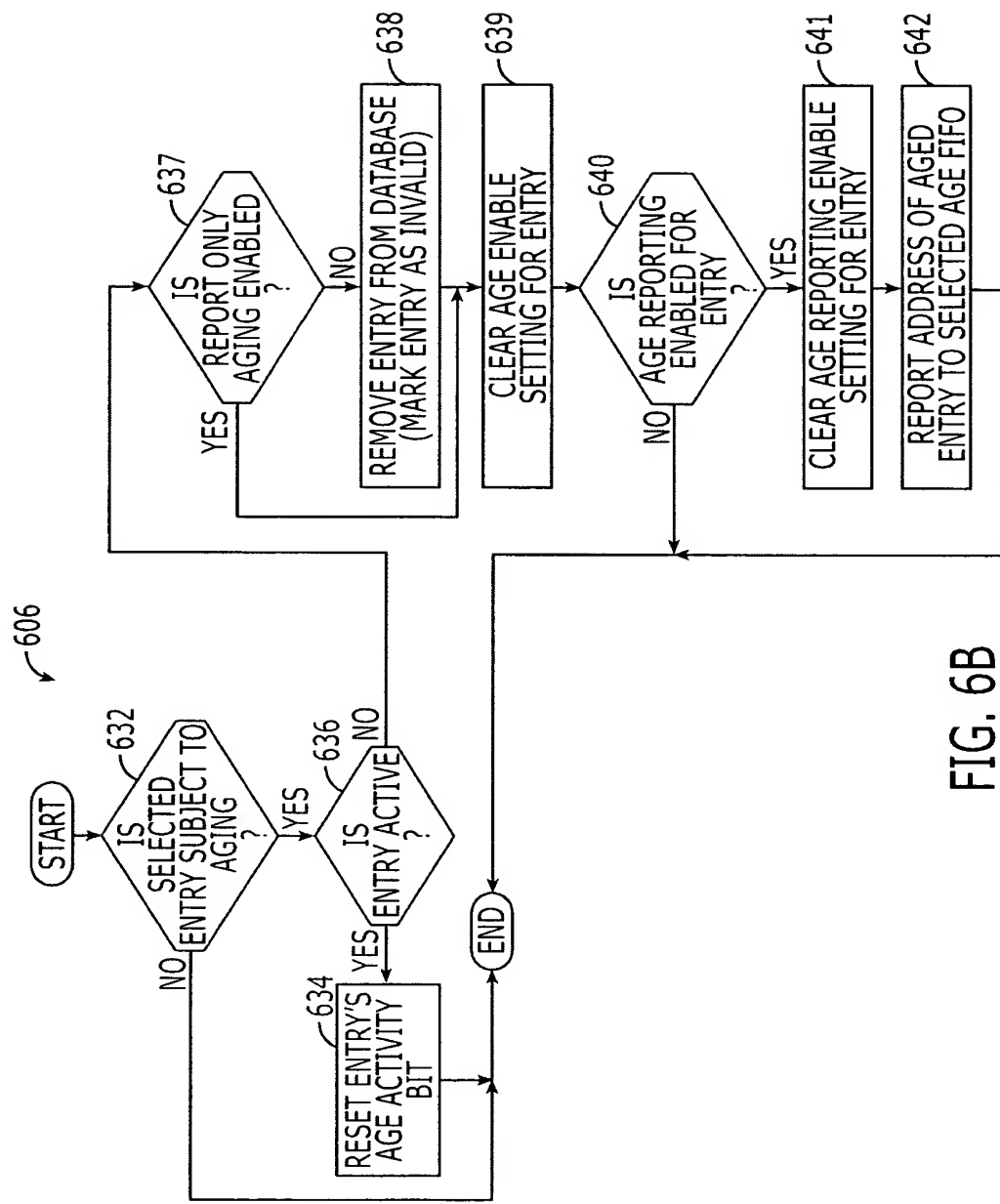


FIG. 6B

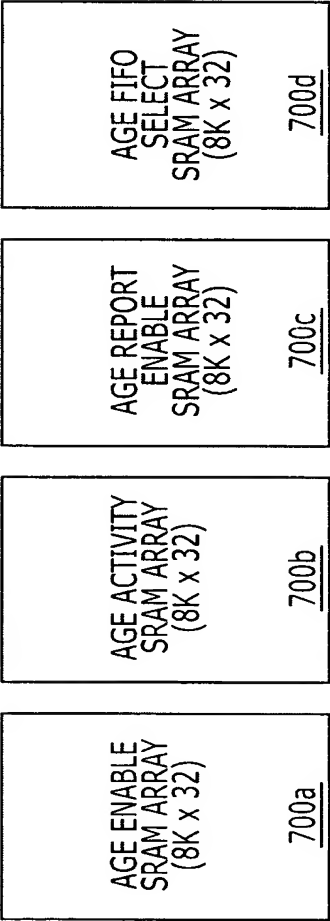


FIG. 7A

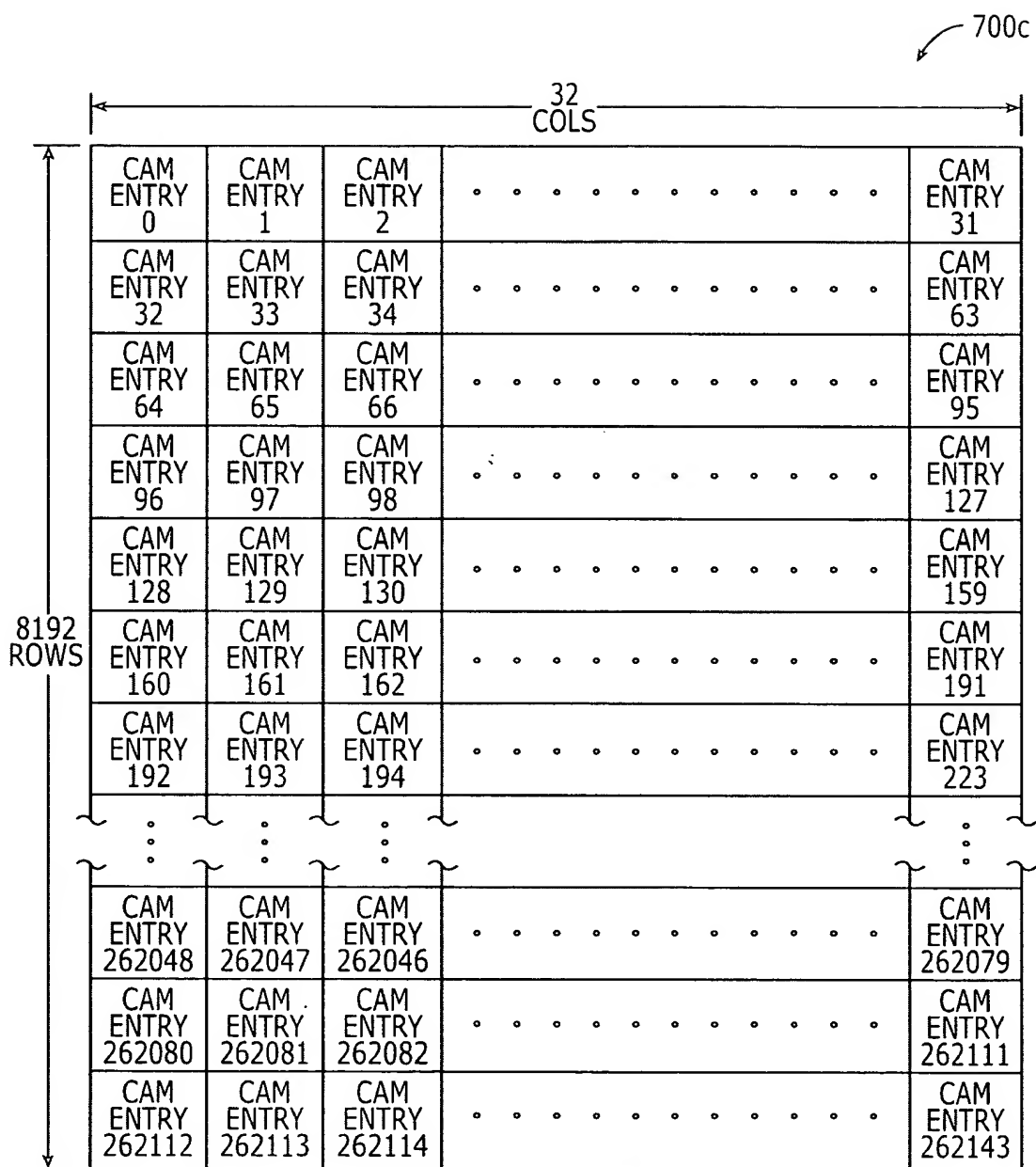


FIG. 7B

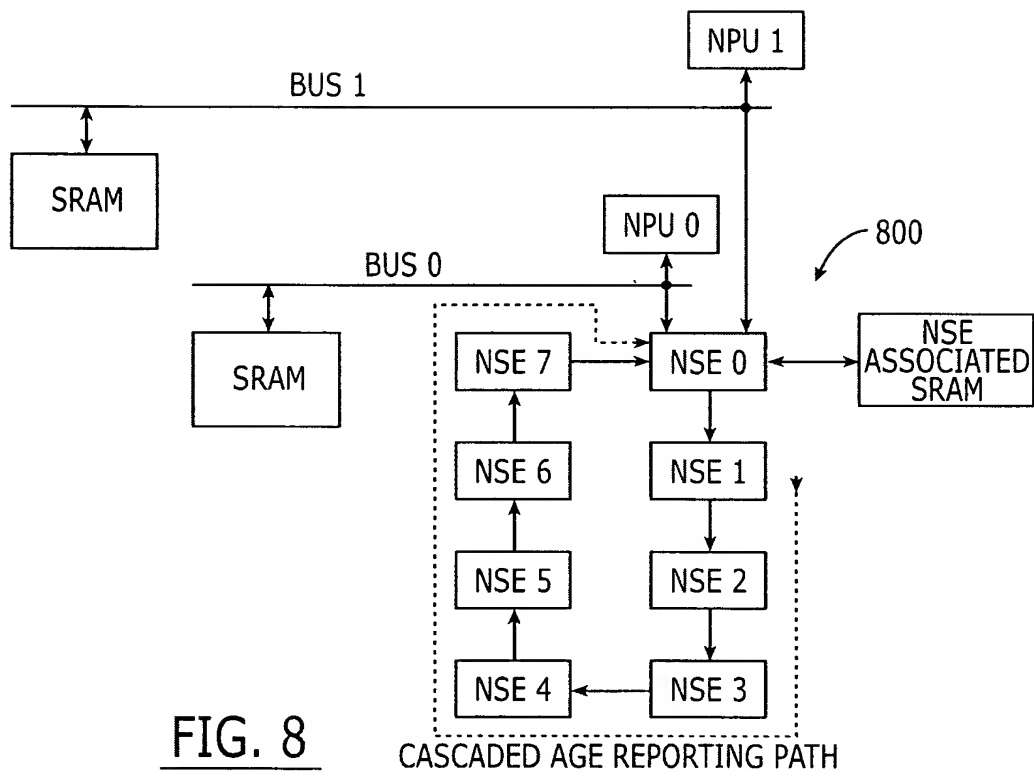


FIG. 8

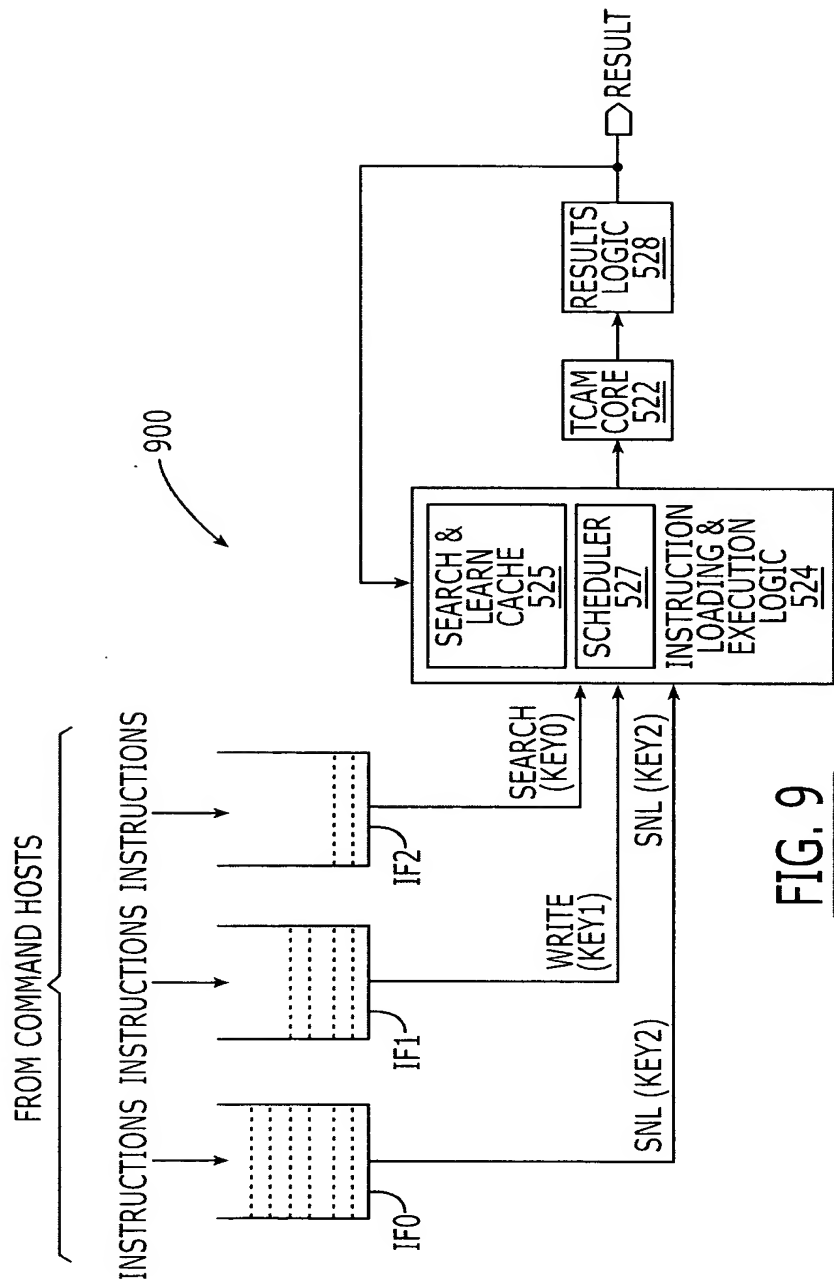


FIG. 9

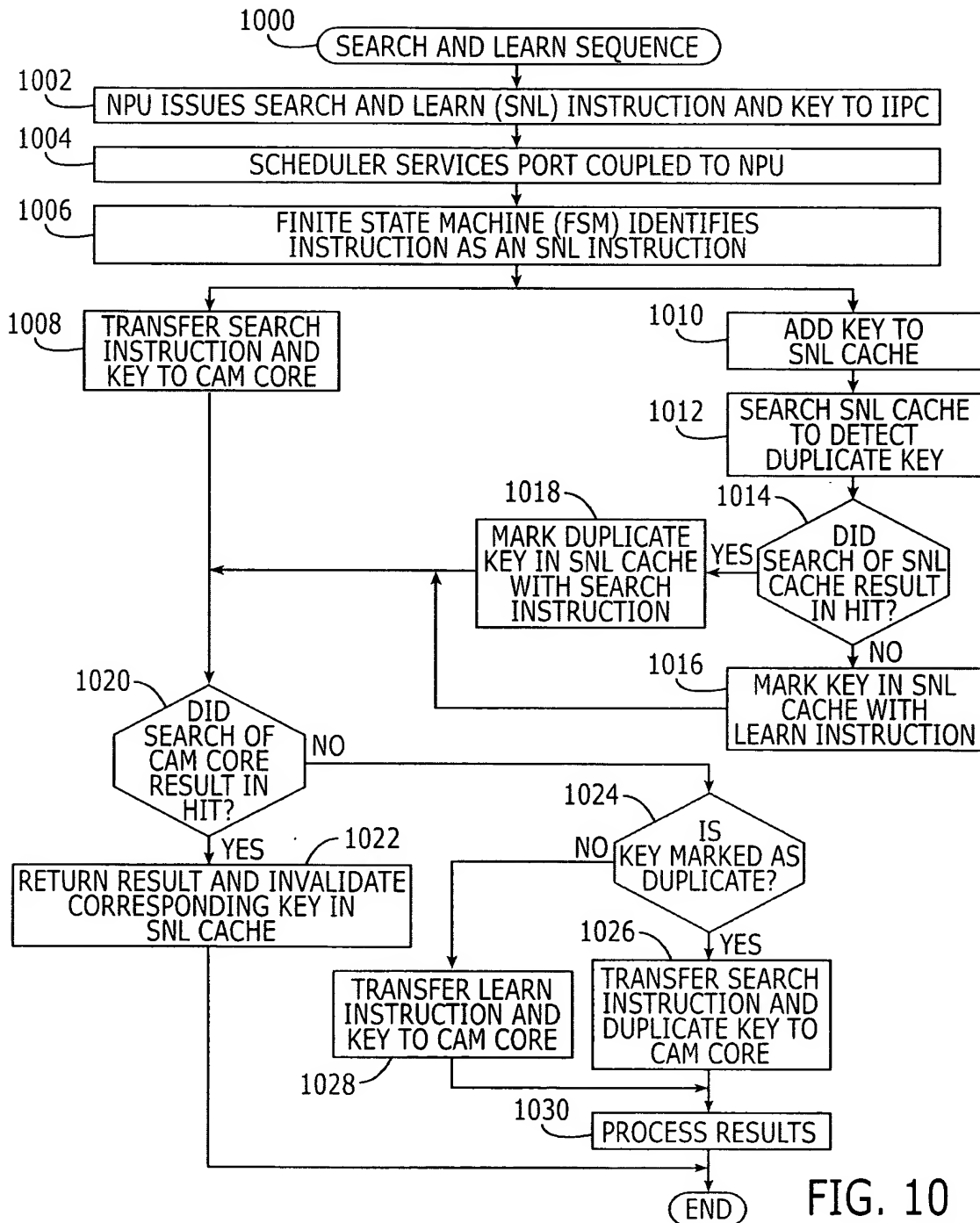


FIG. 10

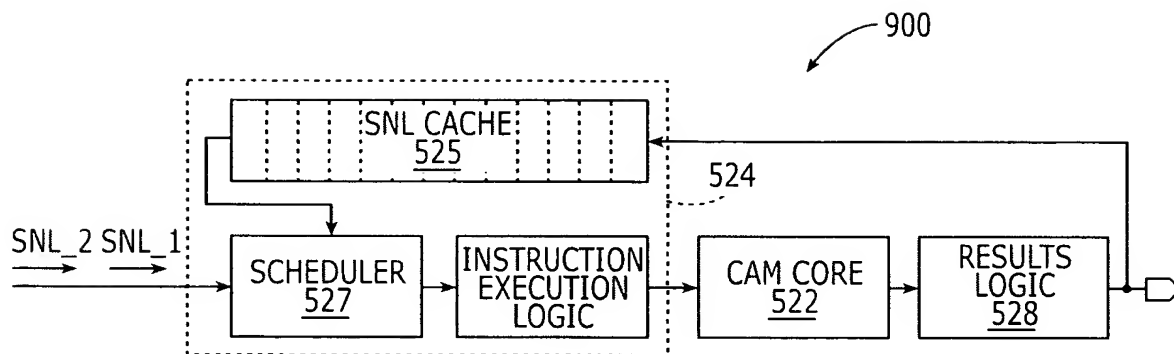


FIG. 11A

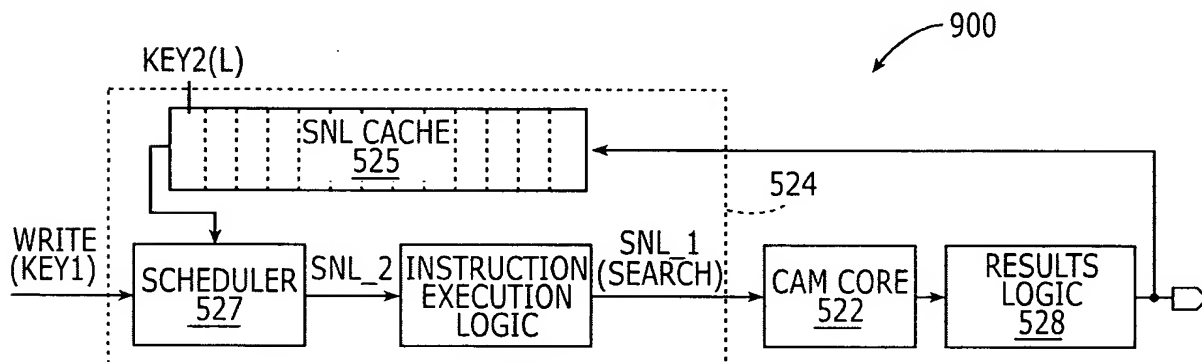


FIG. 11B

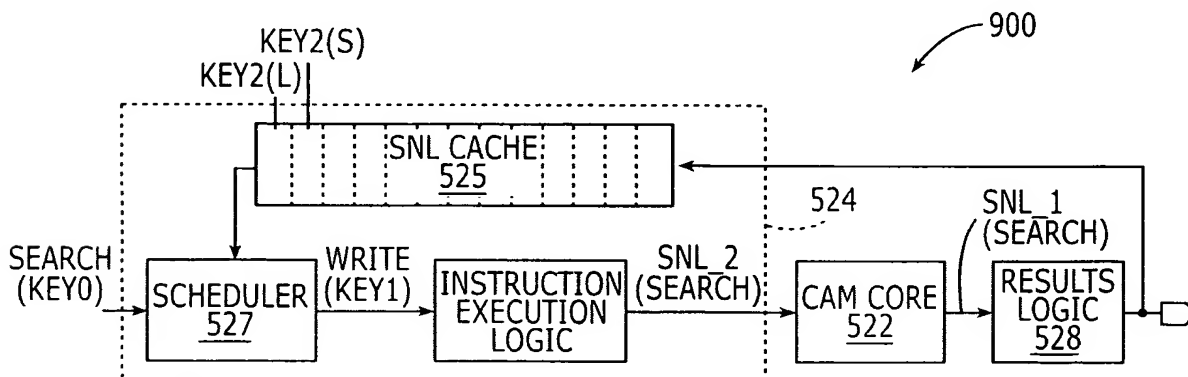


FIG. 11C

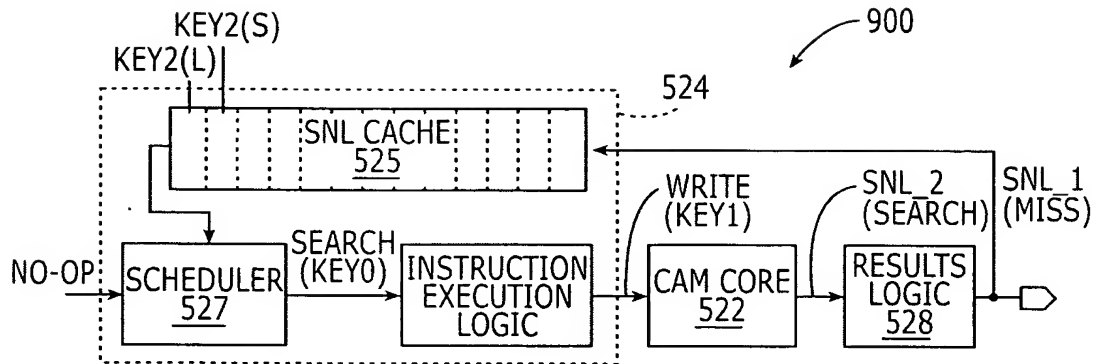


FIG. 11D

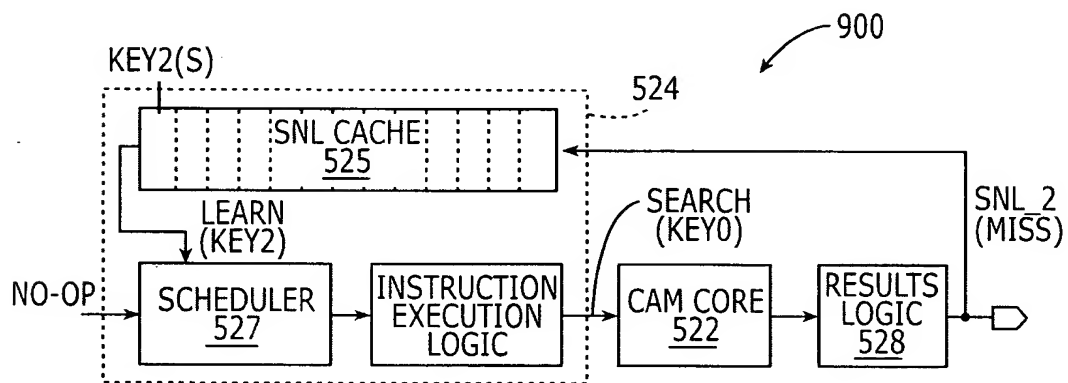


FIG. 11E

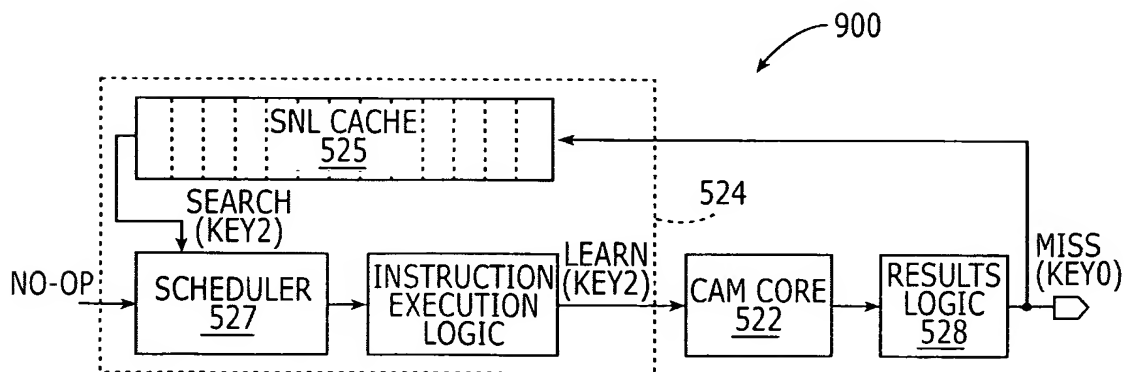


FIG. 11F

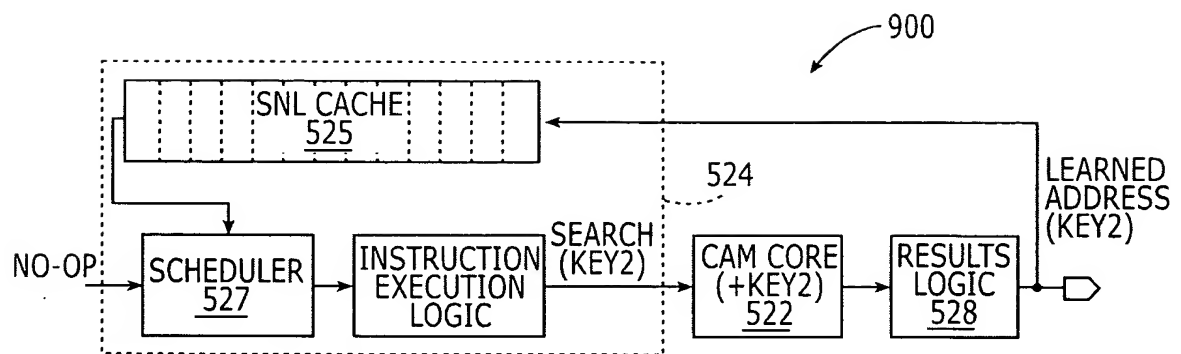


FIG. 11G

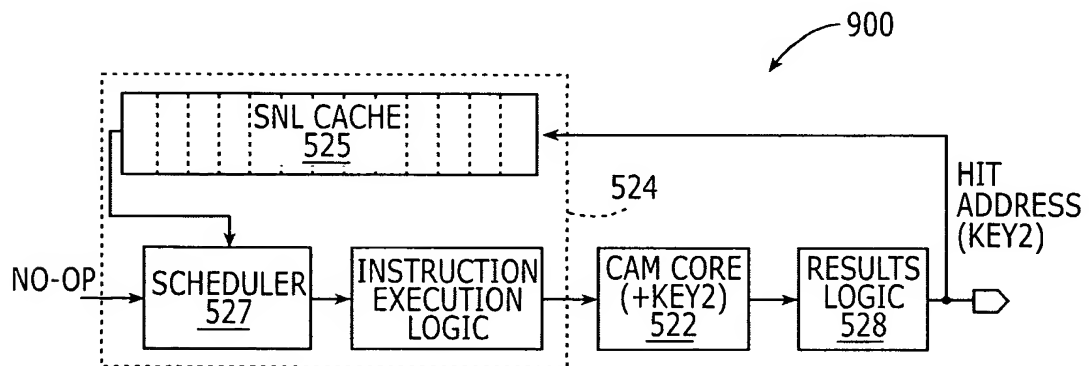


FIG. 11H

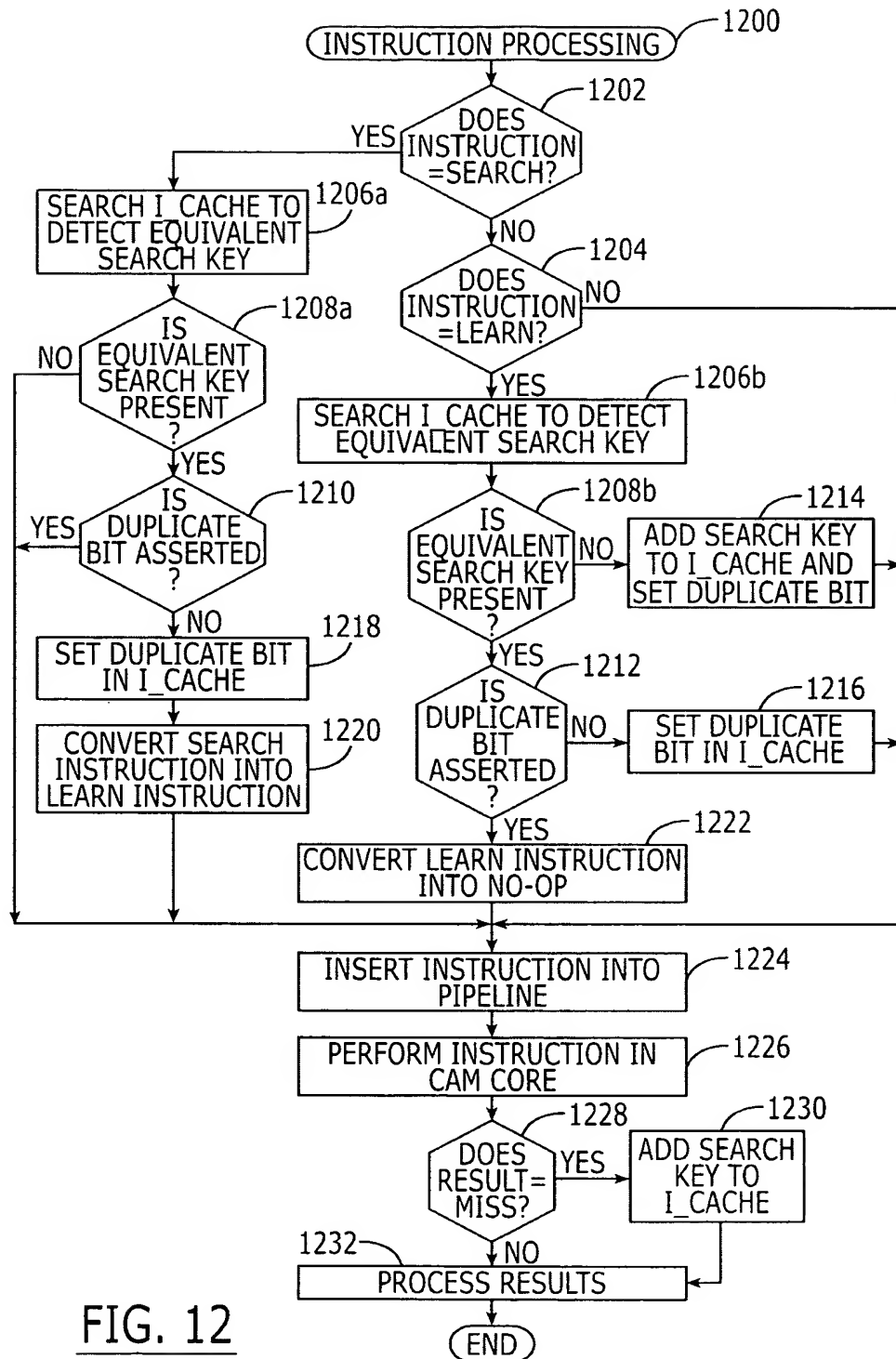


FIG. 12

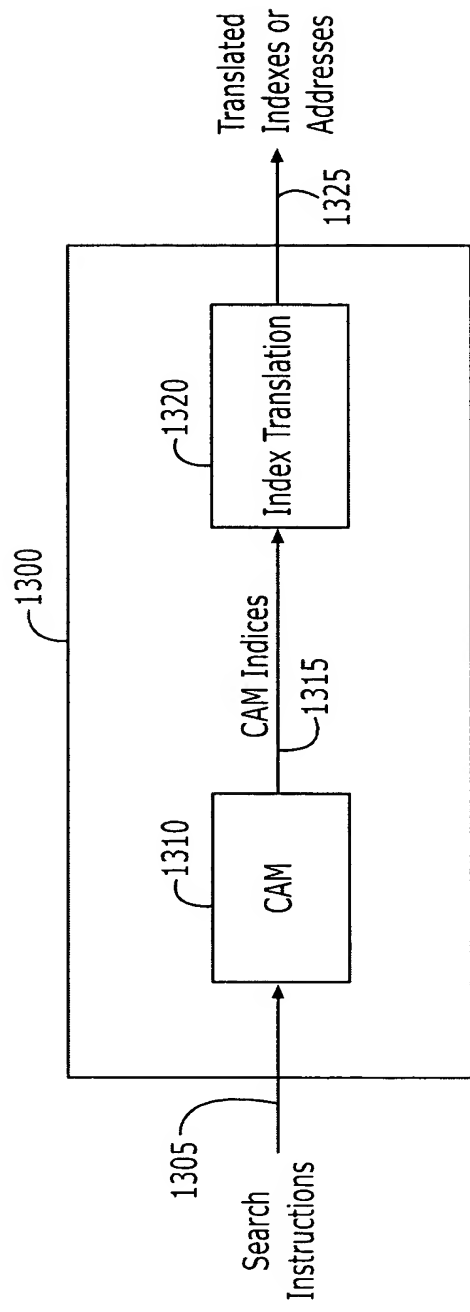


FIG. 13

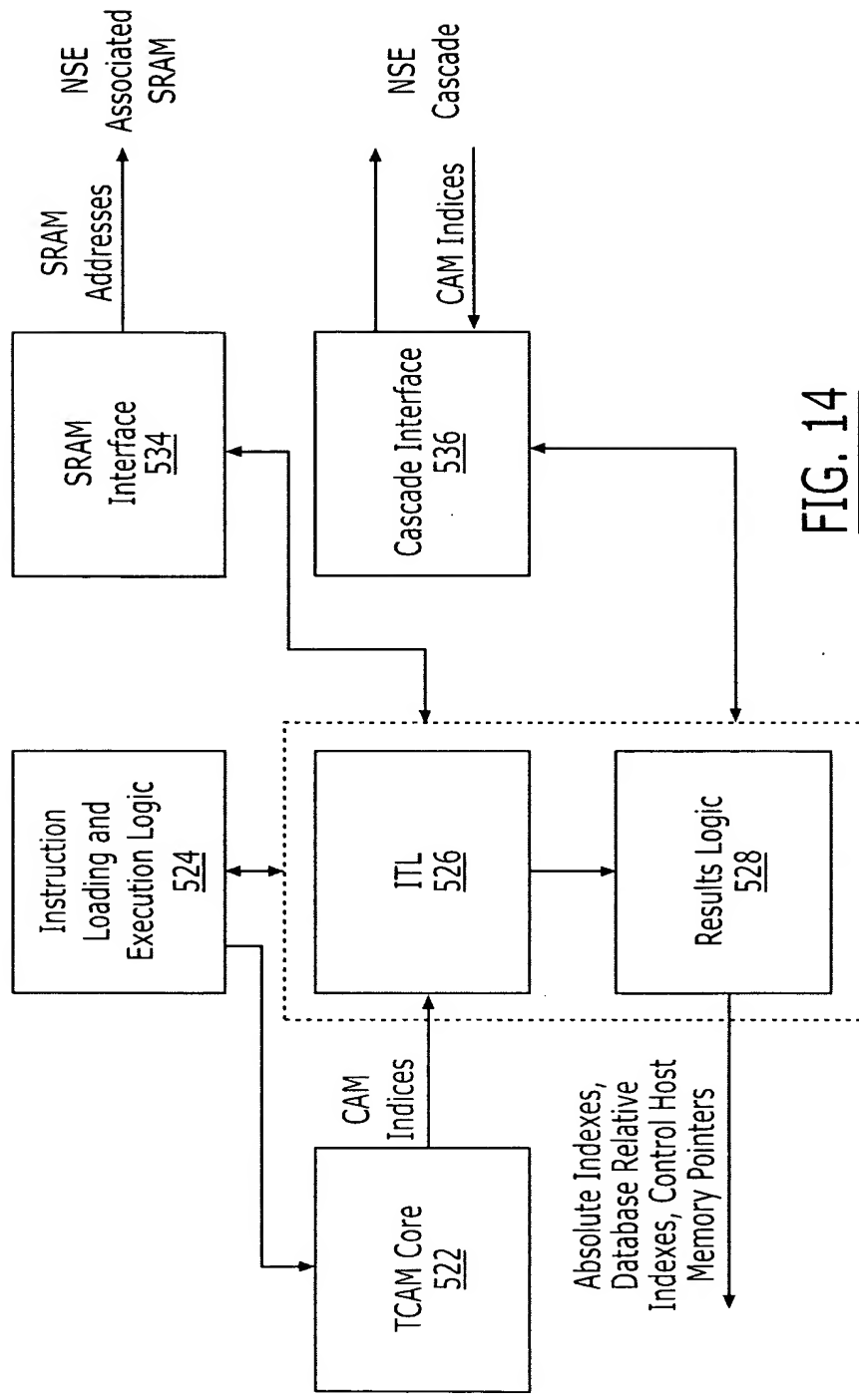


FIG. 14

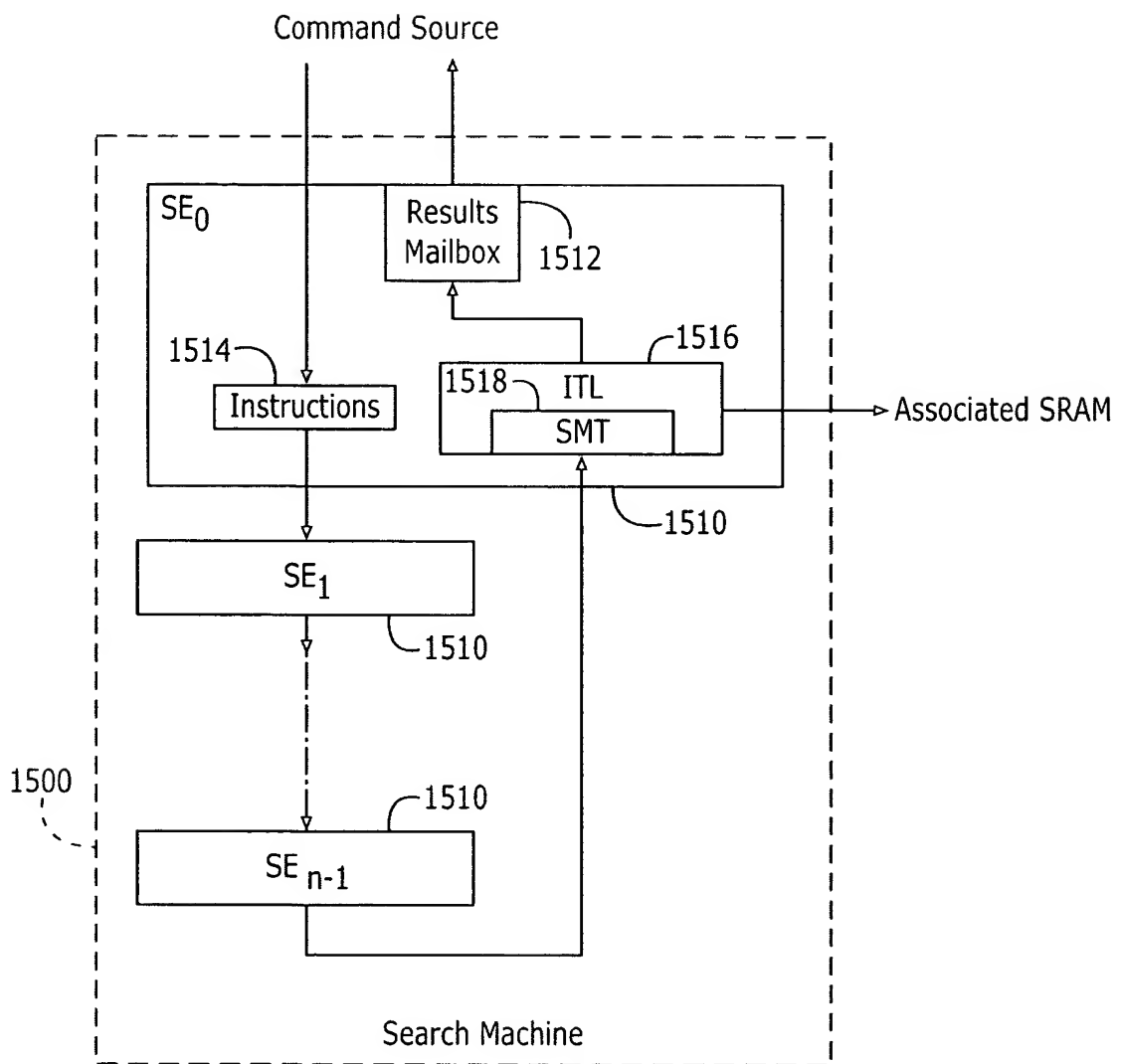


FIG. 15

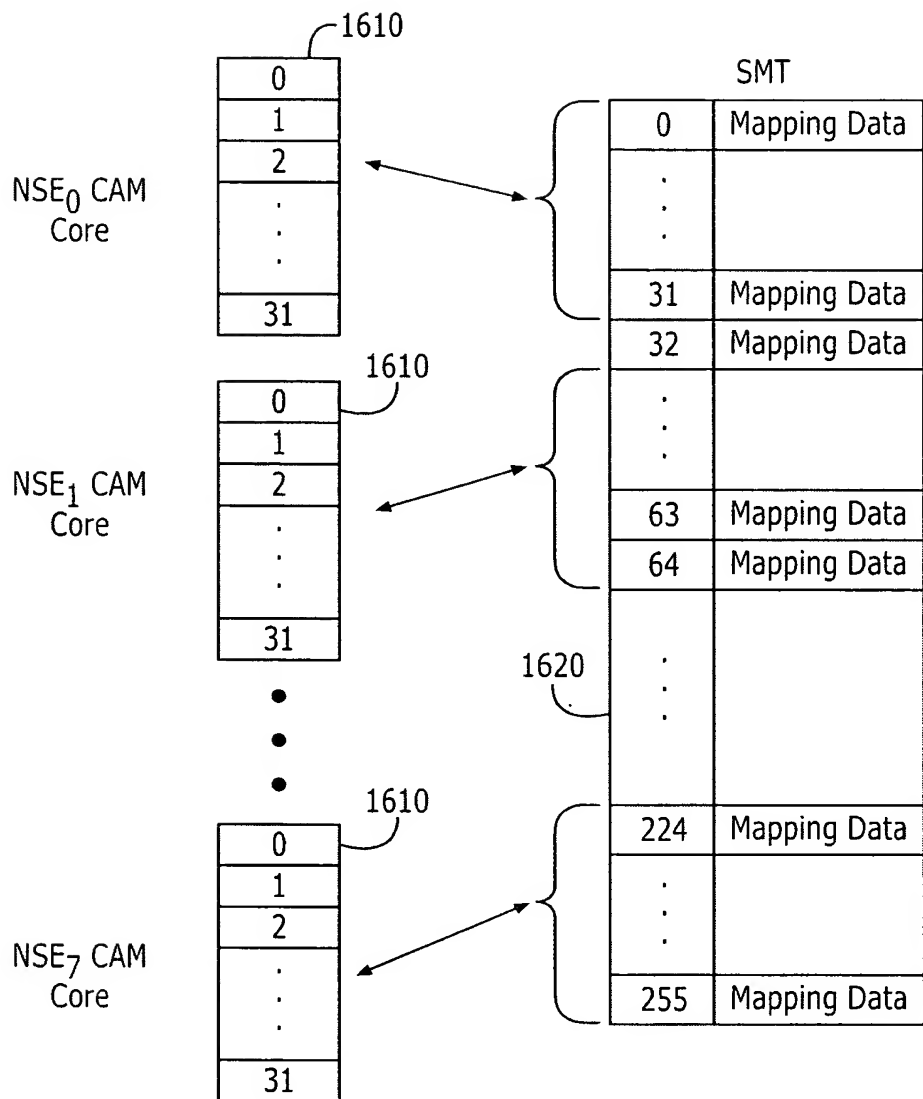


FIG. 16

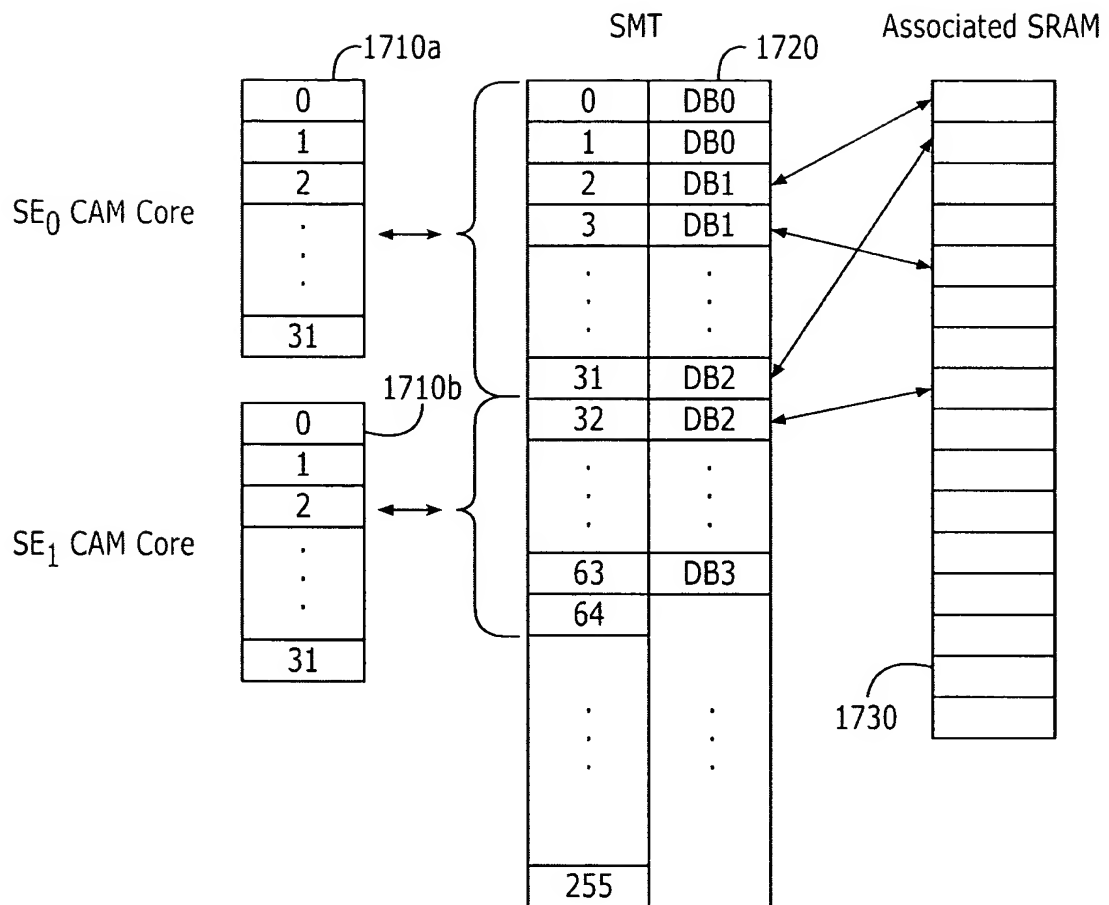


FIG. 17

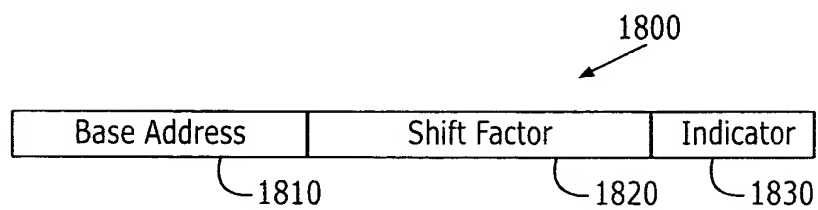


FIG. 18

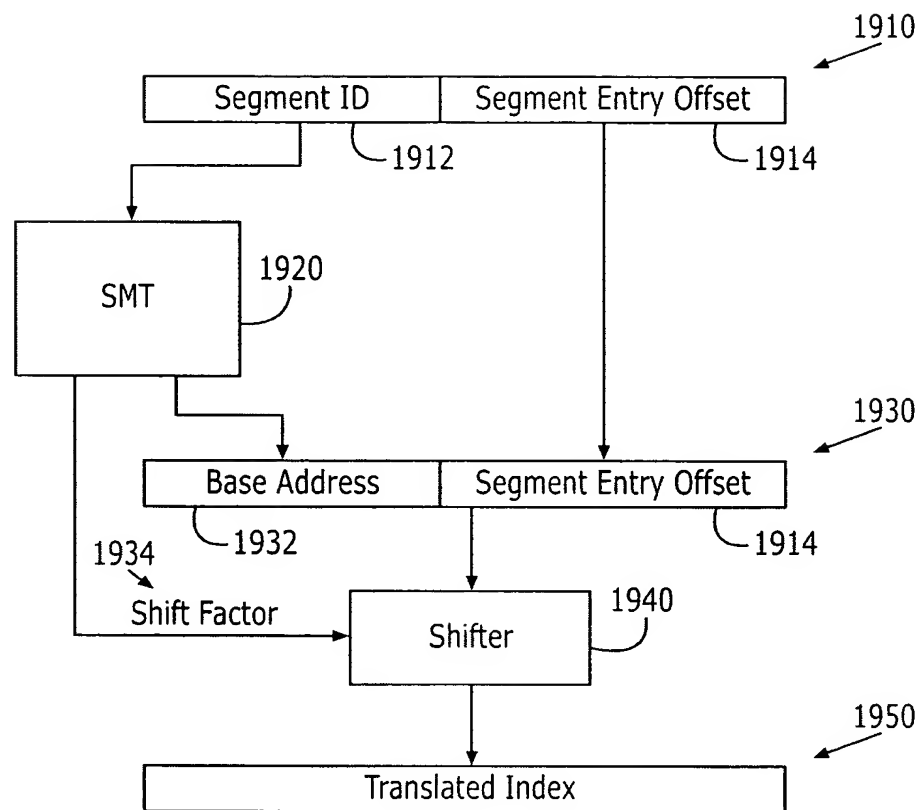


FIG. 19

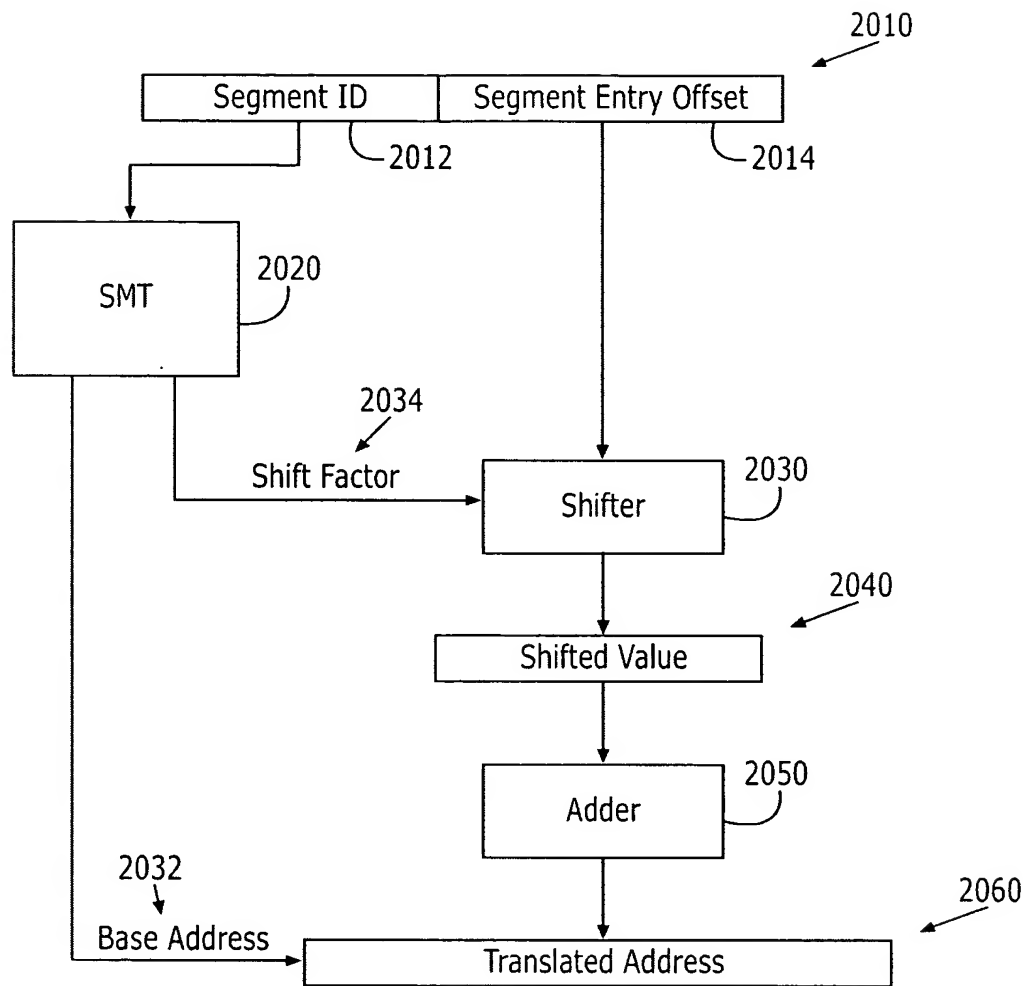


FIG. 20

Bits	24:6	5:2	1	0
Field	BASE_ADDRESS	SHIFT_FACTOR	RSVD	RESULT_TYPE

FIG. 21

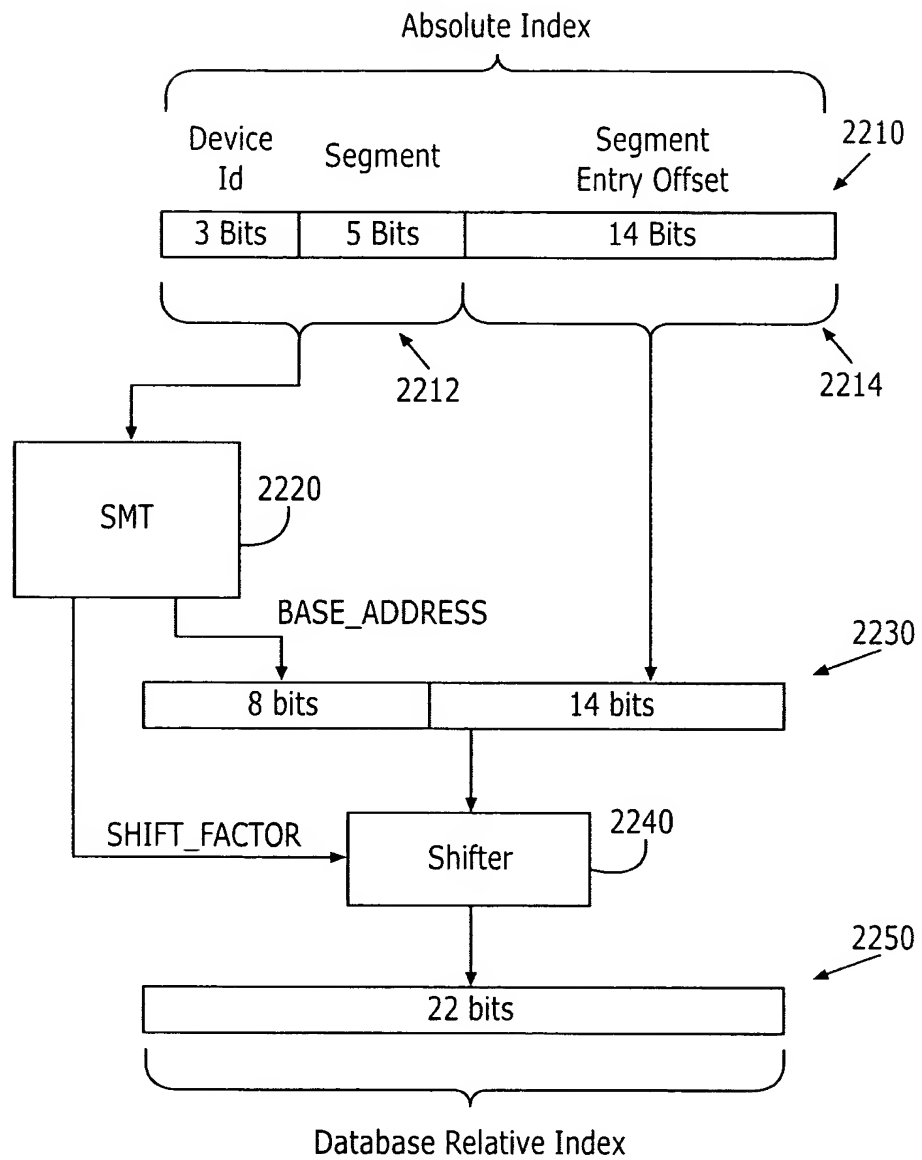


FIG. 22

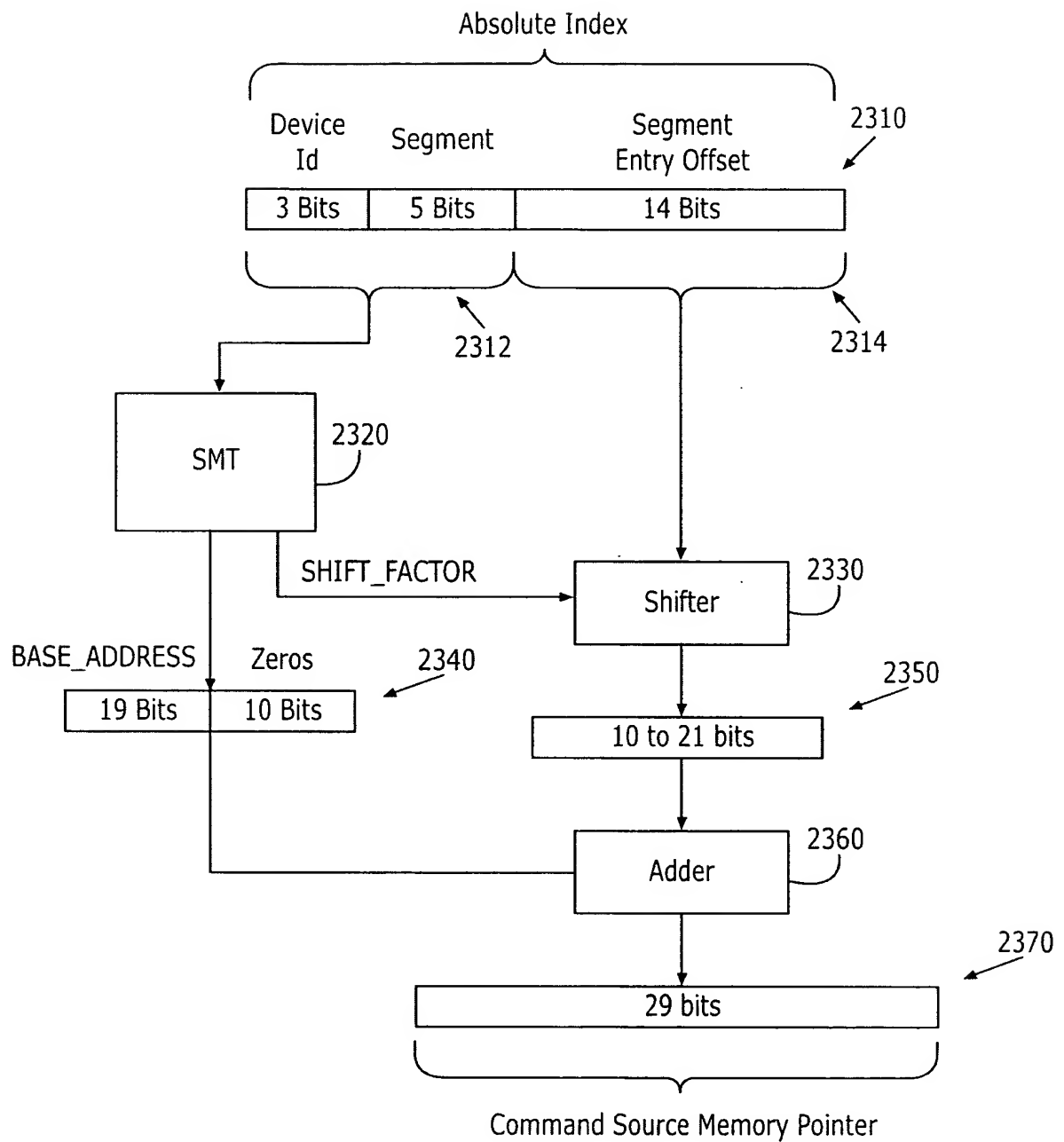


FIG. 23

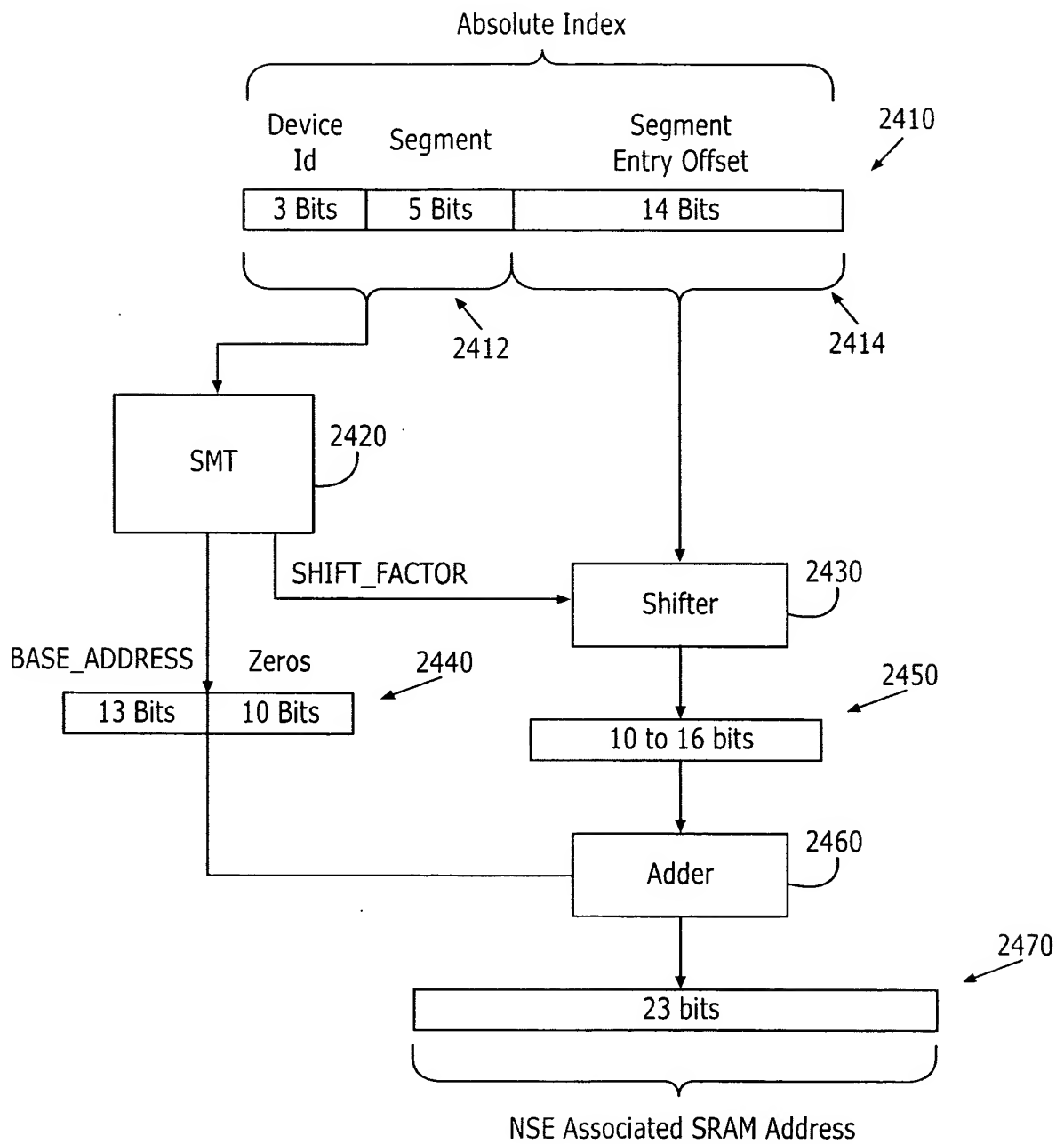


FIG. 24